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Yongin-city, Kyungki-do (KR).

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(74) Agent: **YOU ME PATENT & LAW FIRM**; Teheran  
Bldg., 825-33, Yoksam-dong, Kangnam-ku, 135-080  
Seoul (KR).

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(71) Applicant (*for all designated States except US*): **SAMSUNG ELECTRONICS CO., LTD. [KR/KR]**; 416, Maetan-dong, Paldal-ku, 442-370 Suwon-city, Kyungki-do (KR).

(72) Inventors; and

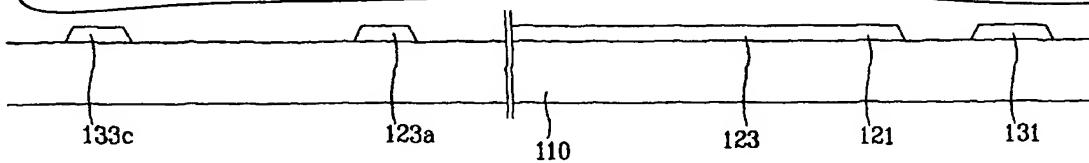
(75) Inventors/Applicants (*for US only*): **KIM, Hee-Seob [KR/KR]**; Shinyoungtong Hyundai Apt. 110-304, 865-1, Banwol-ri, Taean-eup, Hwasung-kun, 445-973 Kyungki-do (KR). **YANG, Young-Chol [KR/KR]**; Jookong Apt. 2-danji 220-1201, Kumjeong-dong, 435-050 Kunpo-city, Kyungki-do (KR). **KIM, Jong-Lae [KR/KR]**; 145-6, Sungnae 2-dong, Kangdong-ku, 134-841 Seoul (KR). **SHIN, Kyong-Ju [KR/KR]**; Samjeong Seonbi

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(54) Title: **MULTI-DOMAIN LIQUID CRYSTAL DISPLAY AND A THIN FILM TRANSISTOR SUBSTRATE OF THE SAME**



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(57) Abstract: A thin film transistor array panel is provided, which includes; an insulating substrate; a plurality of gate lines formed on the insulating substrate; a plurality of data lines formed on the insulating substrate, insulated from the gate lines, and intersecting the gate lines; a plurality of storage electrode lines formed on the insulating substrate, insulated from the data lines, and intersecting the data lines; a plurality of pixel electrodes provided on the respective pixel areas defined by the intersections of the gate lines and the data lines, each pixel electrode having a cutout; a plurality of direction control electrodes provided on the respective pixel areas defined by the intersections of the gate lines and the data lines; a first thin film transistor connected to a relevant one of the gate lines, a relevant one of the data lines and a relevant one of the pixel electrodes; a second thin film transistor connected to a previous one of the gate lines, a previous one of the data lines and a relevant one of the direction control electrodes; an a third thin film transistor connected to the previous gate line, the relevant data line and the relevant pixel electrode.